



Patent Attorney's Docket No. <u>030682-066</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Atsushi MIYANISHI et al.) Group Art Unit: 2815
Application No.: 09/114,203	Examiner: B. Baumeister
Filed: July 13, 1998	Examiner: B. Baumeister CEIVED RECEIVED RECEIVED
For: SEMICONDUCTOR DEVICE	JUL 27 2000 TECHNOLOGY CENTER 2800
AMENDMENT/REPLY TI	RANSMITTAL LETTER
Assistant Commissioner for Patents Washington, D.C. 20231	
Sir:	
Enclosed is a reply for the above-identified par	tent application.
[X] A Petition for Extension of Time is also	enclosed.
[] A Terminal Disclaimer and a check for requisite Government fee are also enclosed.	[] \$55.00 (248) [] \$110.00 (148) to cover the sed.
[] Also enclosed is	·
[] statement(s) claiming small ent [] are also enclosed [] were submitted	
[] Applican (s) request continued examinat \$345.00 (279) [] \$690.00 (179) fee due ut	ion under 37 C.F.R. § 1.114 and enclose the [] nder 37 C.F.R. § 1.17(e).
[] Applicant(s) previously submitted _ requested.	, on, for which continued examination is
[] A Request for Entry and Consideration (146/246) is also enclosed.	of Submission under 37 C.F.R. § 1.129(a)
[] No additional claim fee is required.	

[X] An additional claim fee is required, and is calculated as shown below:

	No. OF CLAIMS	HIGHEST NO. OF CLAIMS PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	ADDT'L FEE
Total Claims	12	MINUS 20 =	0	× \$18.00 (103) =	·
Independent Claims	4	MINUS 3 =	1	× \$78.00 (102) =	\$78.00
If Amendment adds mu	ltiple depende	ent claims, add \$260	.00 (104)		
Total Amendment Fee					· · · · · · · · · · · · · · · · · · ·
If small entity status is	claimed, subt	ract 50% of Total A	mendment Fe	e	
TOTAL ADDITIONA	L FEE DUE	FOR THIS AMEN	DMENT		\$78.00

[X] -A-claim-fee-in-t	he-amount of \$ <u>78.00</u>	is_enclosed.
[] Charge \$	to Deposit Account	No. 02-4800.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(d) and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOAME, SWECKER & MATHIS, L.L.P.

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Registration No. 32,131

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Date: July 21, 2000

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Filed: July 13, 1998 For: SEMICONDUCTOR DEVICE AMENDMENT UNDER 37 C.F.R. §1.111 Assistant Commissioner for Patents TECHNOLOGY CENTER 2800				
Assistant Commissioner for Potents	TECHNOLOGY CEL			

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

This amendments responds to the Office Action dated April 6, 2000 (Paper No. 7).

Please amend the above-noted application as follows:

IN THE CLAIMS:

Please amend claim 1 and add new claim 12 as follows:

Claim 1, line 13, change "the length" to --a length--.

--12. (New) A method of fabricating a semiconductor device on the basis of a layout design, the layout design achieved by a process comprising the steps of:

designing a layout of an active area on a plane, said active area being defined from an insulating film by a boundary including a first edge extending along a first direction and a second edge extending along the first direction and a third edge connected between one ends of the first and second edges extending along a second direction different from the

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